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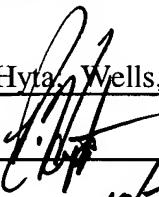
Total Number of Pages in This Submission

Application Number	10/630,427
Filing Date	July 29, 2003
First Named Inventor	Wendell P. Noble
Group Art Unit	Unknown
Examiner Name	Unknown
Total Number of Pages in This Submission	Attorney Docket Number MI22-2378

ENCLOSURES (check all that apply)

<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached	<input type="checkbox"/> Assignment Papers (for an Application) <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition Routing Slip (PTO/SB/69) and Accompanying Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation <input type="checkbox"/> Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Small Entity Statement <input type="checkbox"/> Request for Refund	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Additional Enclosure(s) (please identify below): PTO Return Postcard; Form 1449; and cited art.
<input checked="" type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/ Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53		
Remarks		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Robert C. Hyta, Wells, St. John, P.S.
Signature	
Date	10/26/03

CERTIFICATE OF MAILING

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EV317134631



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/630,427
Filing Date July 29, 2003
Inventor Wendell P. Noble
Assignee Micron Technology, Inc.
Group Art Unit Unknown
Examiner Unknown
Attorney's Docket No. MI22-2378
Title: Semiconductor Processing Methods of Forming Integrated Circuitry, Forming Conductive Lines, Forming a Conductive Grid, Forming a Conductive Network, Forming an Electrical Interconnection to a Node Location, Forming an Electrical Interconnection With a Transistor Source/Drain Region, and Integrated Circuitry

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References –See Attached Form PTO-1449

The Examiner's attention is directed to the reference which is listed on the attached Form PTO-1449, a copy of which is attached. No admission is made regarding whether the submitted reference is prior art.

Citation of the referenced art is respectfully requested.

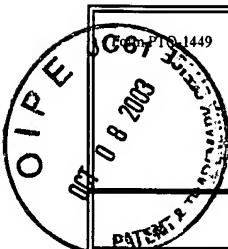
This Supplemental Information Disclosure Statement is being filed before the mailing date of a first Office Action. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

Dated: 16/7/03

By: 

Robert C. Hyta
Reg. No. 46,791



U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE					ATTY. DOCKET NO. MI22-2378		SERIAL NO. 10/630,427with		
					APPLICANT Wendell P. Noble				
					FILING DATE July 29, 2003		GROUP Unassigned		
U.S. PATENT DOCUMENTS									
*Examiner Initial		Document Number	Date	Name		Class	Subclass	Filing Date If Appropriate	
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